



Japan Regional Standards Committee (JRSC)

Liaison Report

Nahoko Koga / SEMI Japan December, 2025

STANDARDS

CONNECT - COLLABORATE. - INNOVATE. - GROW. - PROSPER

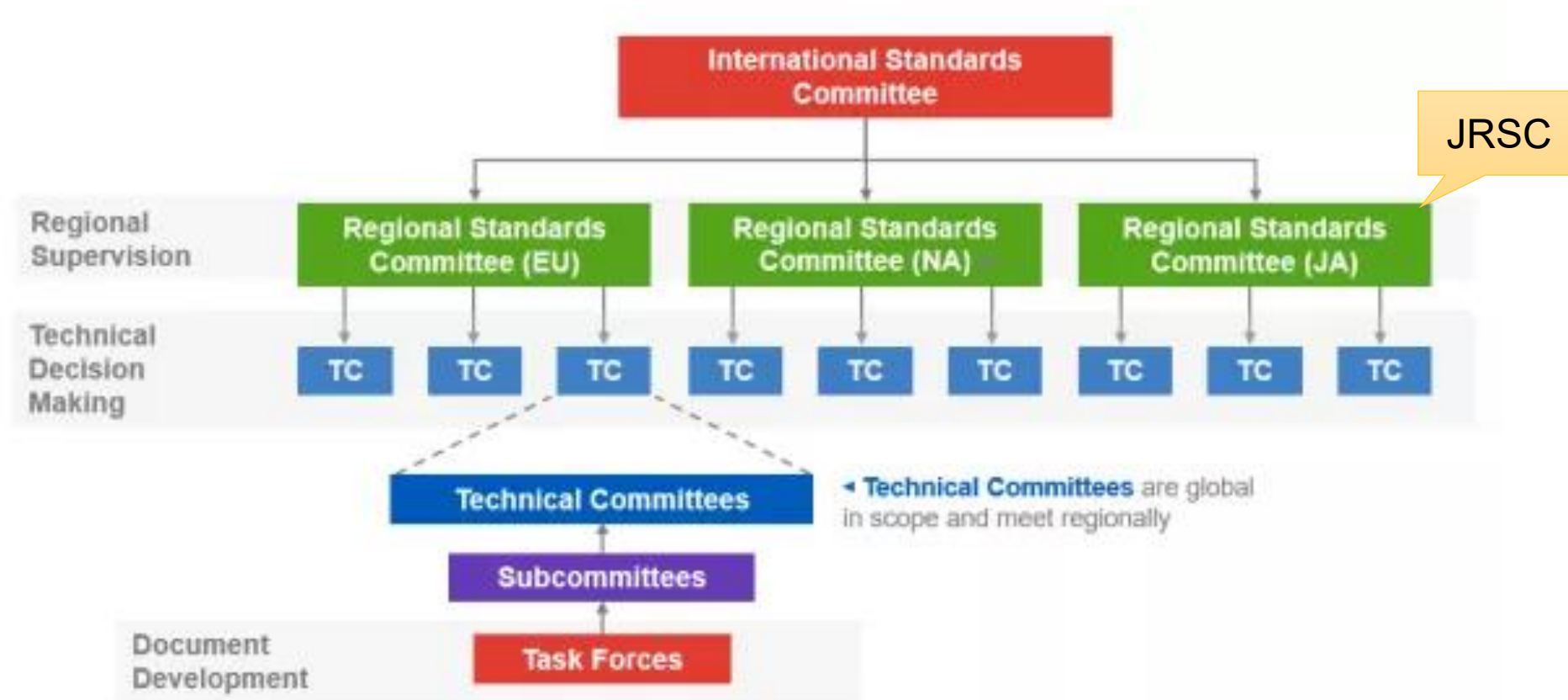
AGENDA

Subtitle

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SEMI Standards Organization

Committee Structure



* China, Korea, and Taiwan Standards activities supported by North America Region

SEMI Japan RSC Organization



JRSC Leadership

- Co-chairs
 - Ryuji Takeda – GlobalWafers Japan
 - Daisuke Sado – Eagle Industry
- Vice-chair
 - Supika Mashiro – Tokyo Electron Limited

JRSC Meeting Information

Subtitle

- Last meeting
 - Thursday, August 28, 2025 11:00-13:30
 - TODA Hall & Conference
- Next meeting
 - Monday, December 15, 2025 14:00 - 17:00
 - SEMI Japan, Tokyo, Japan / Official Virtual TC Chapter Meeting (Hybrid)

Topics

SEMI Japan Standards Awards Overview

Awards Name	Reasons for Recognition	Organization to Select/ Approve
SEMI Japan Standards Award	<ul style="list-style-type: none"> Improvement of standards processes Enhancement and dissemination of standards in the industry Broad regional impact 	Based on recommendation by the Standards Award Selection Committee, the JRSC approves
SEMI Japan International Collaboration Award	<ul style="list-style-type: none"> Overcoming language/cultural barriers International coordination Promoting SEMI Standards globally 	
SEMI Japan Honor Award	<ul style="list-style-type: none"> Establishment/reorganization of committees Long-term service (approx. 4+ years) 	
SEMI Japan Special Award	Significant contributions in special fields, equivalent to Standards/International Collaboration Awards	
Technical Committee Award	<ul style="list-style-type: none"> Leadership in document development- Significant contributions as lead developer Committee management Task force coordination- Standards dissemination 	Selected by Technical Committee Chairs, reported to JRSC Chairs

Technical Committee Award

- The JRSC revised the Awards Guidelines for the **“Technical Committee Award”**
 - The Technical Committee Award is presented to individuals who have made outstanding contributions to their respective Technical Committees in the Japan Region.
 - To date, the Guidelines have not permitted the same individual to receive the Technical Committee Award more than once for the same document or activity, even if the activity spans multiple years.
 - The Guideline was revised to allow individuals to be recognized for their significant contributions in a given year, even if the same work continues over multiple years.
- Announcement of Award Recipients
 - Until now, recipients of the Technical Committee Award have been recognized during the TC meetings held in conjunction with SEMICON Japan or the subsequent nearest meeting, and the recognition has been recorded in the meeting minutes.
 - However, the JRSC decided that award recipients will also be featured on a display panel at the SEMICON Japan venue.

Planning Meeting Updates

Planning Meeting 2025 (1/2)

Title: Gaps in Supply Chain Transparency and SEMI Standards

Date & Time: Thursday, August 28, 2025 14:00–17:30

Venue: Room 403+404, TODA HALL & CONFERENCE

Organizer: Japan Regional Standards Committee (JRSC)

Participants: 20 (TC Chairs, TF Leaders, and Program Members recommended by TC Chairs or TF Leaders)

Overview:

In recent years, the increasing geopolitical risks and frequent natural disasters have heightened concerns about the stability of the supply chain in the microelectronics industry. Under such circumstances, ensuring transparency across the entire supply chain is gaining attention as a key to enhancing stability and resilience. However, despite the growing emphasis on transparency, various gaps—both large and small—still exist in reality. In this meeting, we will introduce the activities of SCM and explain specific examples of transparency gaps, such as the transmission of hazardous chemical substance information and risk assessments related to cybersecurity. Based on this information, we will hold a discussion on transparency gaps in the supply chain, visualize the issues, and conduct group work to identify areas that should be considered for standardization, and potential of SEMI Standards.

Planning Meeting 2025 (2/2)

Agenda:

- Opening
- Introduction: Supply Chain Management Initiative under SEMI
- Examples:
 - Contained substances, Supika Mashiro from Tokyo Electron
 - Cyber security, Takayuki Nishimura from SCREEN Semiconductor Solutions
- Brainstorming about gaps in supply chain transparency
- Group Work
- Presentation based on the group work
- Closing

Group Discussion Summary:

- Transparency in the semiconductor supply chain is essential for timely delivery of required quantity of semiconductor products with reasonable cost, product security, and minimizing environmental risks.
- There are challenges in sharing regulatory information, especially across regions and departments, and reluctance to disclose software details due to IP concerns.
- Standardization opportunities include updating SEMI E187 to address new regulations, building shared databases and platforms for cybersecurity and material information, and promoting common terminology and formats.
- Supporting smaller and mid-sized companies and encouraging responsible information sharing through premium SEMI membership were also discussed as potential solutions.
- Achieving effective transparency will require significant effort, especially in harmonizing definitions and practices across diverse supply chain participants.



THANK YOU

STANDARDS